

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: KAJIHARA, et al.
Serial No.: 09/328,910
Filed: June 9, 1999
For: LEADFRAME SEMICONDUCTOR INTEGRATED
CIRCUIT DEVICE USING THE SAME, AND METHOD
OF AND PROCESS FOR FABRICATING THE TWO
Group: 2815
Examiner: J. Clark
Conf. No.: 7455

STATUS INQUIRY
AND
REQUEST FOR INTERVIEW CONFERENCE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

October 12, 2007

Sir:

Applicant respectfully requests to be informed as to the status of the present application, and further, respectfully notes that this reissue application was filed a significant time ago, i.e., see filing date indicated above within the heading information.

In the interest of expediting prosecution of this reissue application, the Undersigned respectfully requests that an in-person interview conference with Supervisory Primary Examiner (SPE) Ken Parker, Examiner Jasmine Clark, and the Special Programs Examiner (SPRE) assigned to handle examination of this application, in attendance, sometime in the beginning of November 2007 (i.e., the Undersigned is on a business trip until the end of October). The Undersigned will

telephone SPE Ken Parker and/or Examiner Clark soon to attempt to schedule the interview conference.

Respectfully submitted,

ANTONELLI, TERRY, STOUT & KRAUS, LLP

/Paul J. Skwierawski/
Paul J. Skwierawski
Registration No. 32,173

PJS/slk
(703) 312-6600